

PATENT NUMBER

<p>O.I.P.E.</p> <p>SCANNED <i>ASZ</i> O.A. <i>TC</i></p>	<p>PATENT DATE</p>
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TS Stephen Jensen

Circuit board having ferrite containing layer

PTO-2040
12/99

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. 1	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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